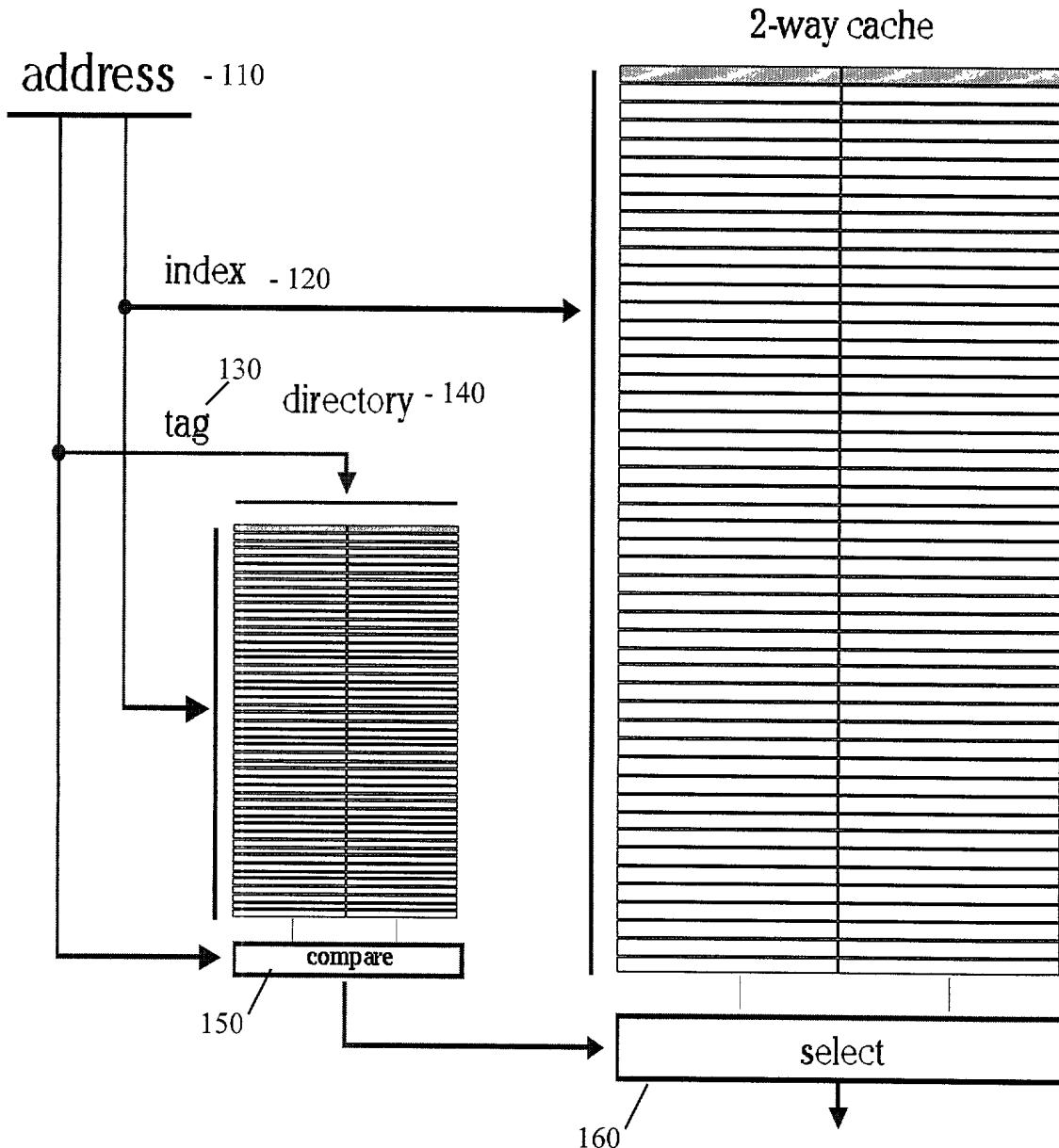
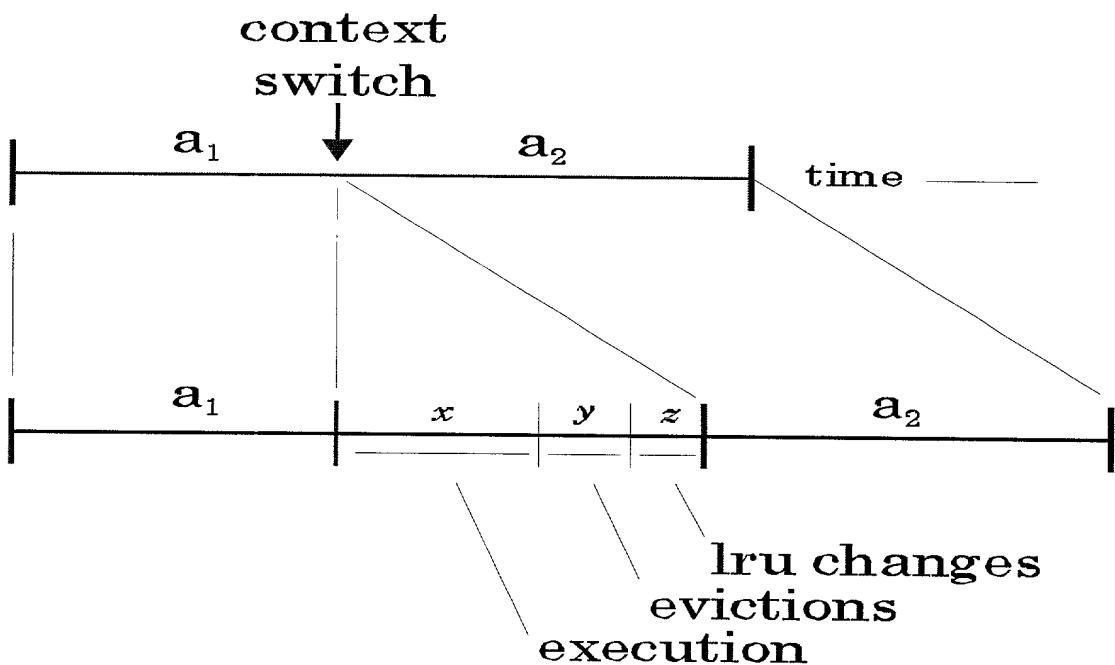


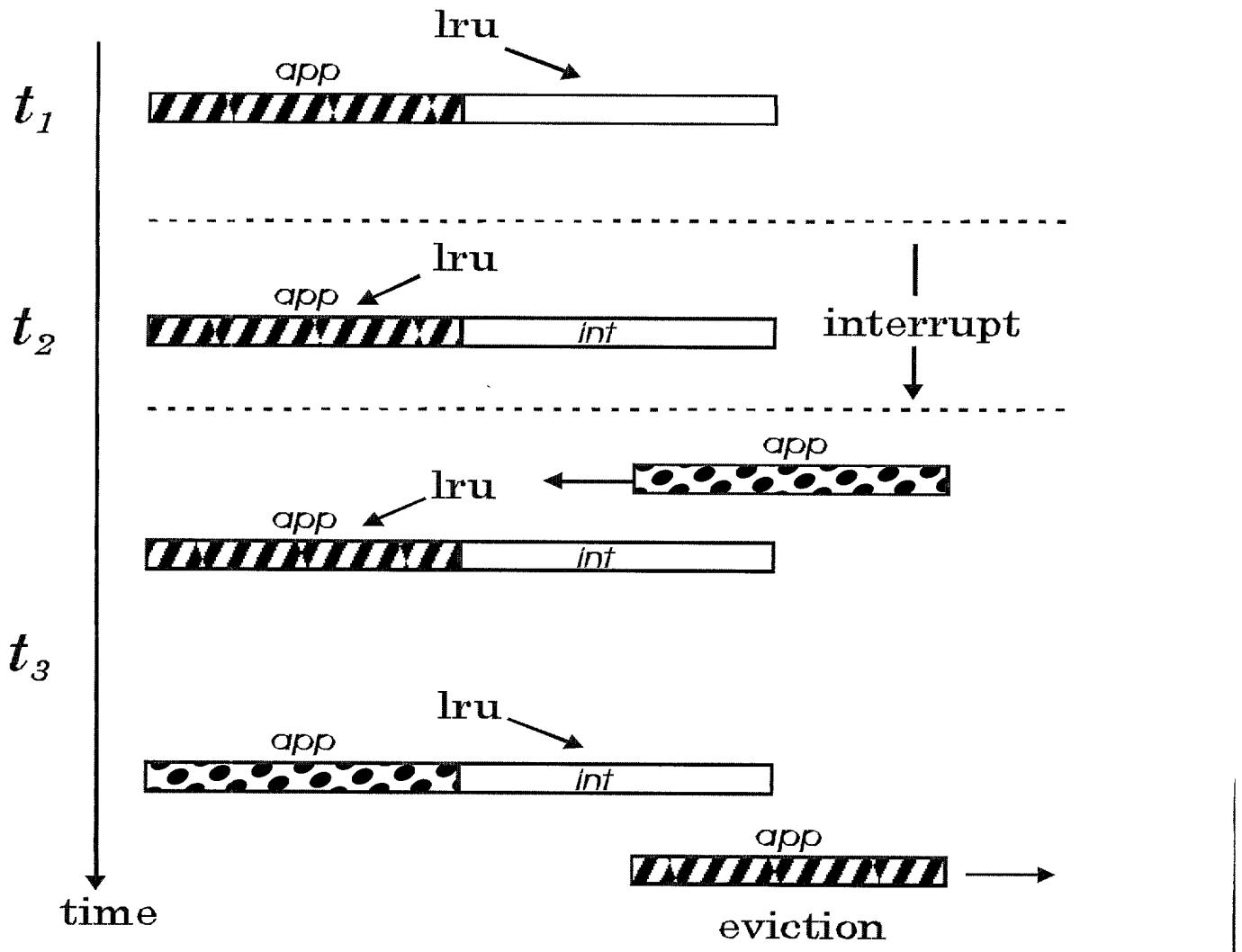
## Two-way Set Associative Cache - 100



**FIG. 1**  
**PRIOR ART**



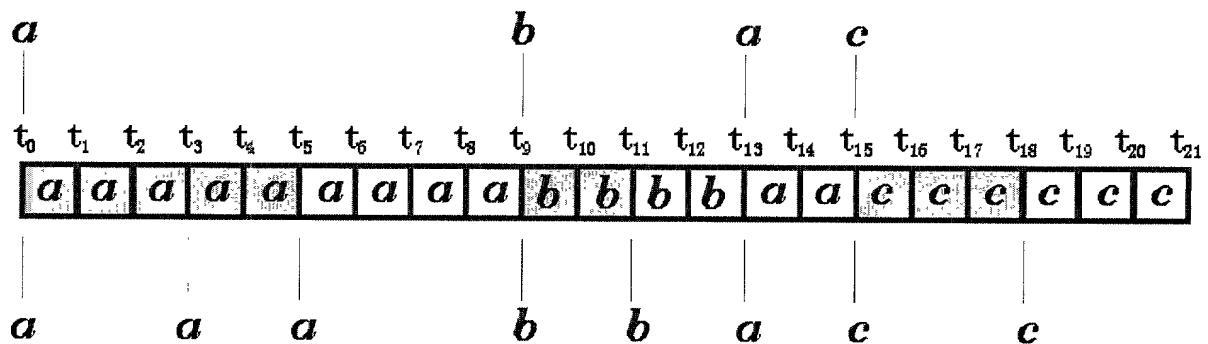
**FIG. 2**



**FIG. 3**

blocks inserted into frame

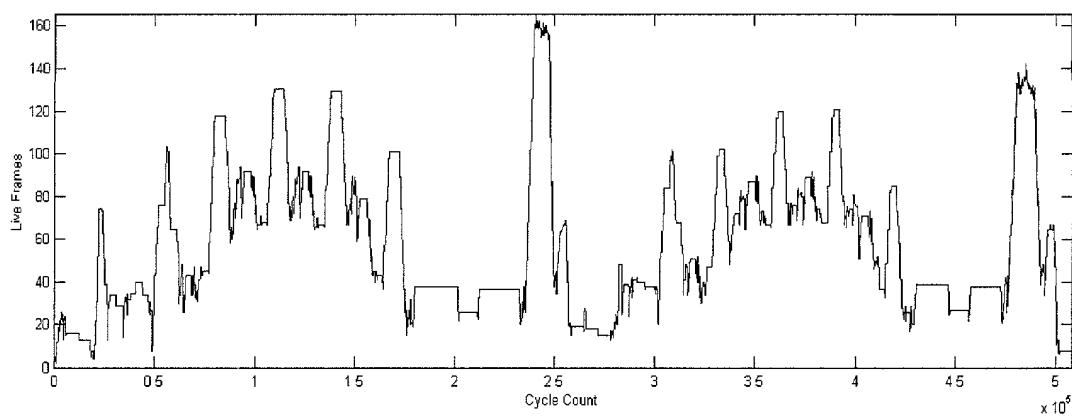
*time* —————



blocks accessed

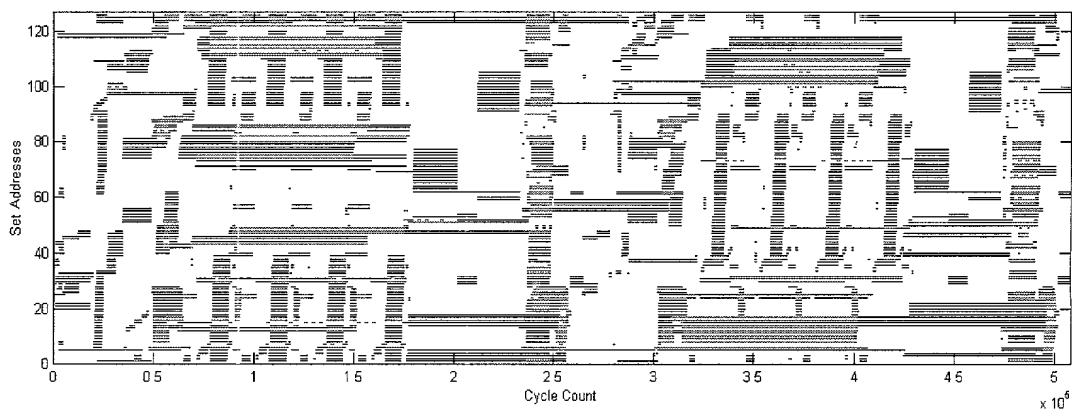
**FIG. 4**

**Live Cache Frames vs. Time (AMR)**



**FIG. 5A**

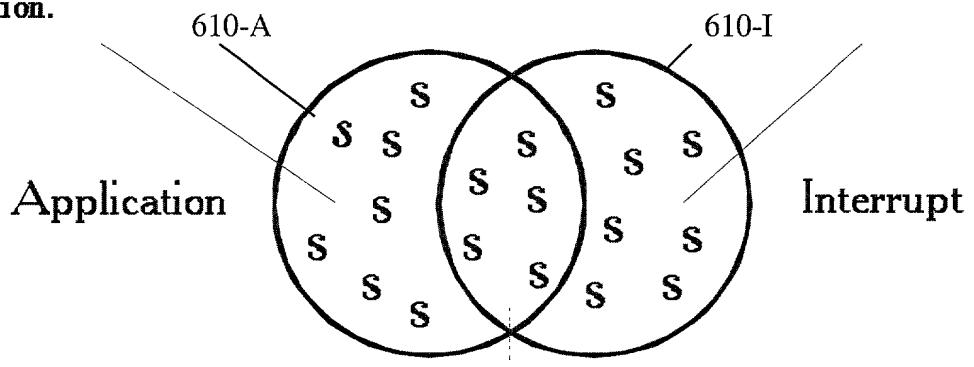
**Live Cache Sets vs. Time (AMR)**



**FIG. 5B**

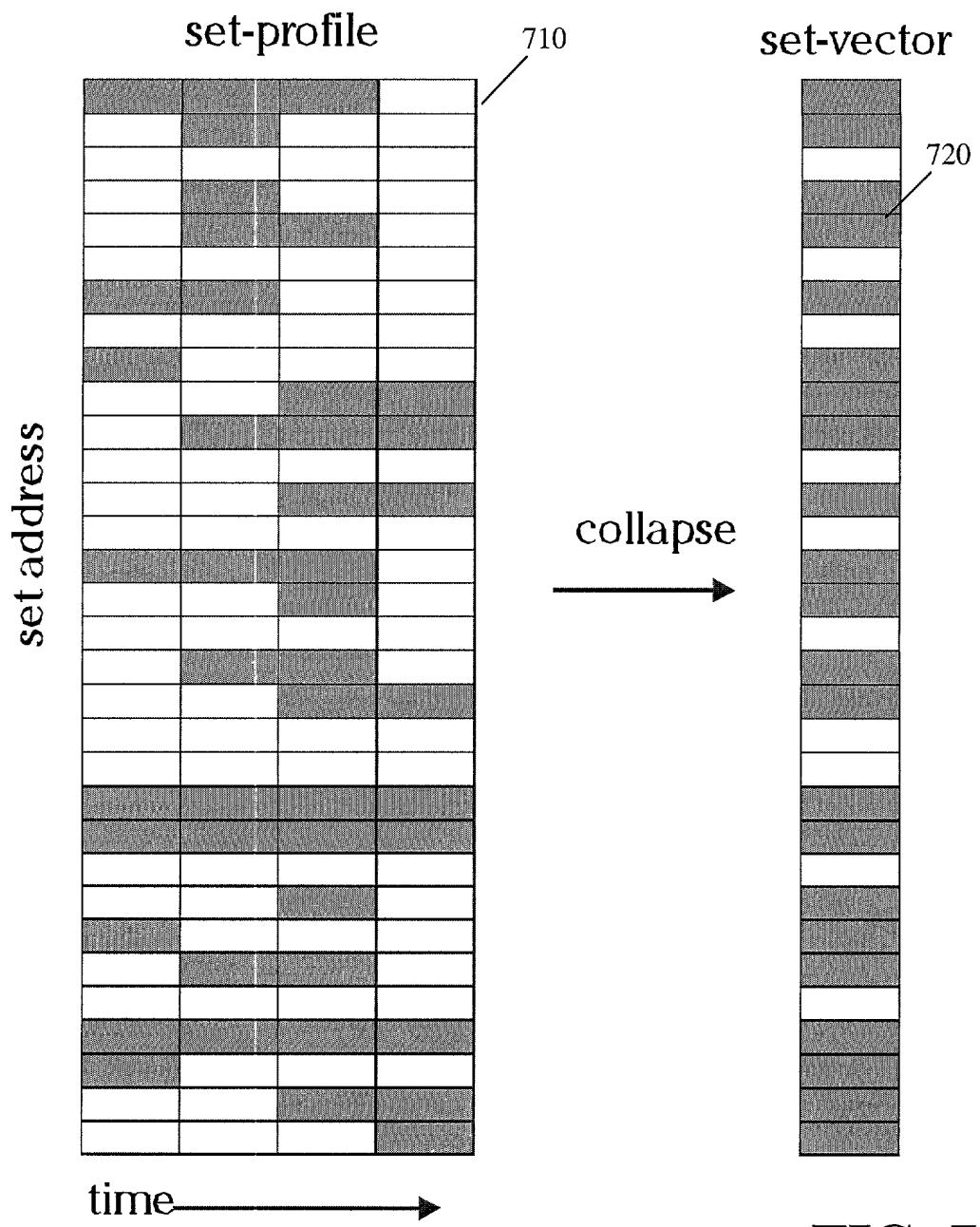
Sets containing a live frame at any time during an application's execution.

Sets occupied by an interrupt at any time during its execution.

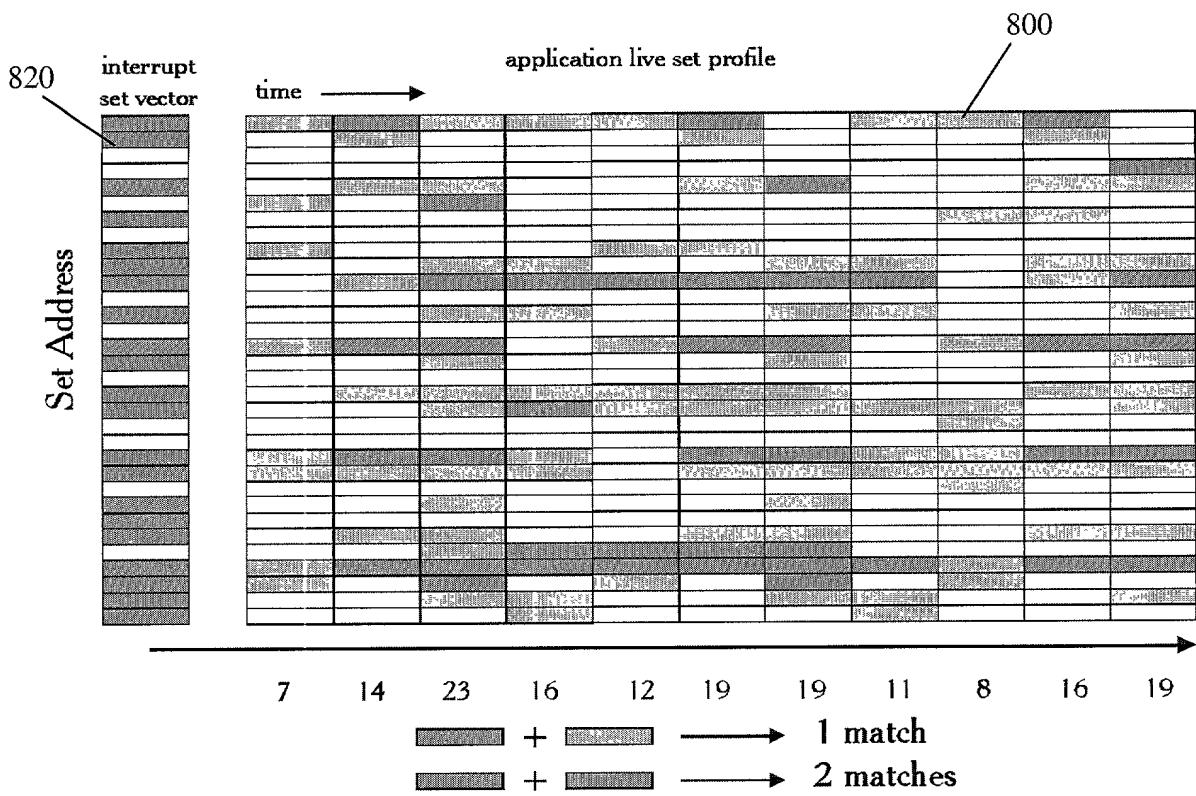


**FIG. 6**

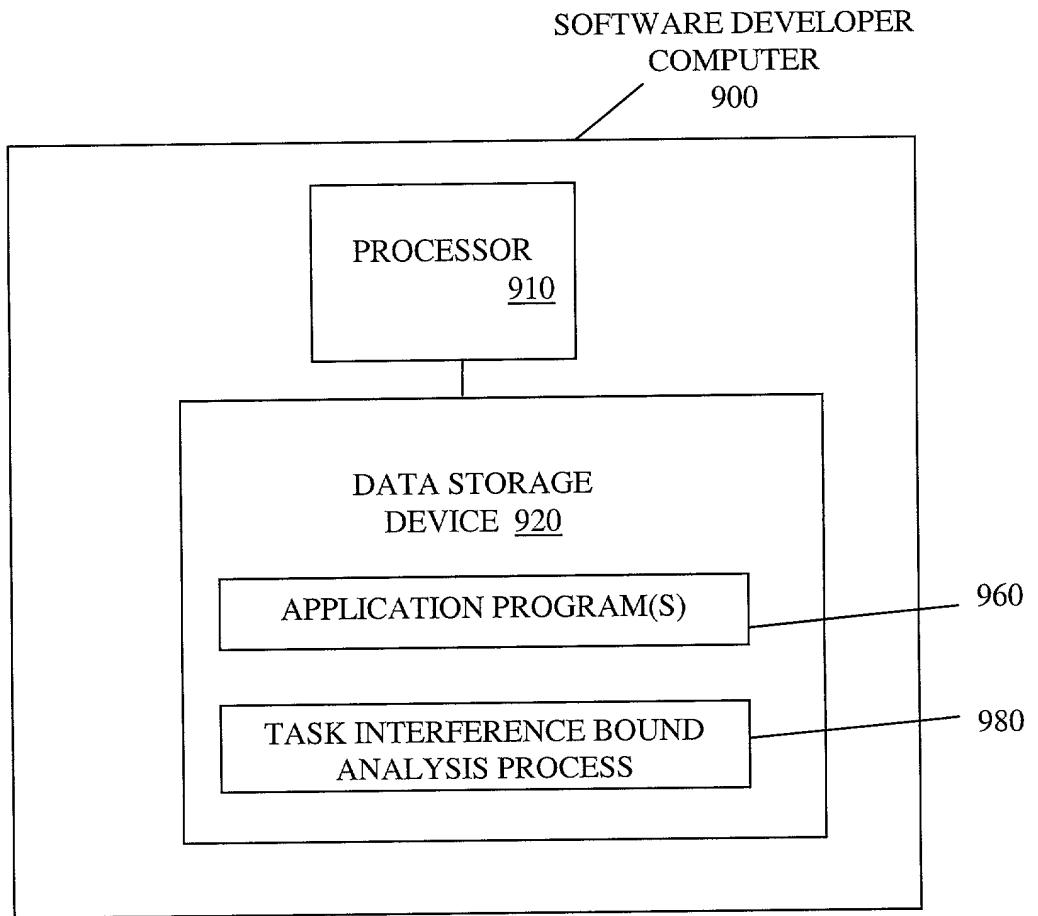
Sets in the intersection may experience the eviction of a block from a live frame.



**FIG. 7**



**FIG. 8**



**FIG. 9**